

FIG. 1

ON-CHIP POWER-ON VOLTAGE INITIALIZATION

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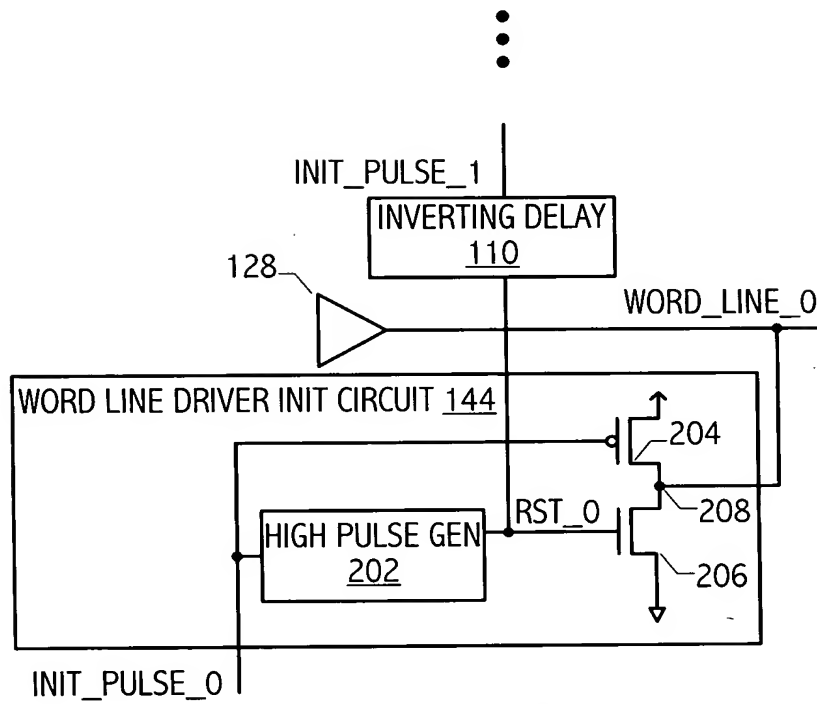


FIG. 2A

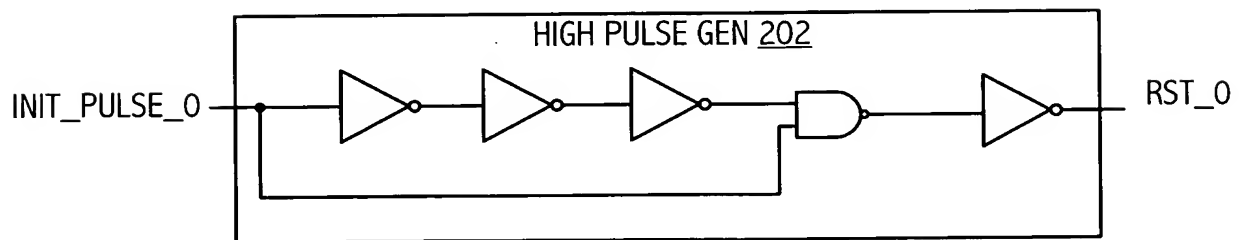


FIG. 2B

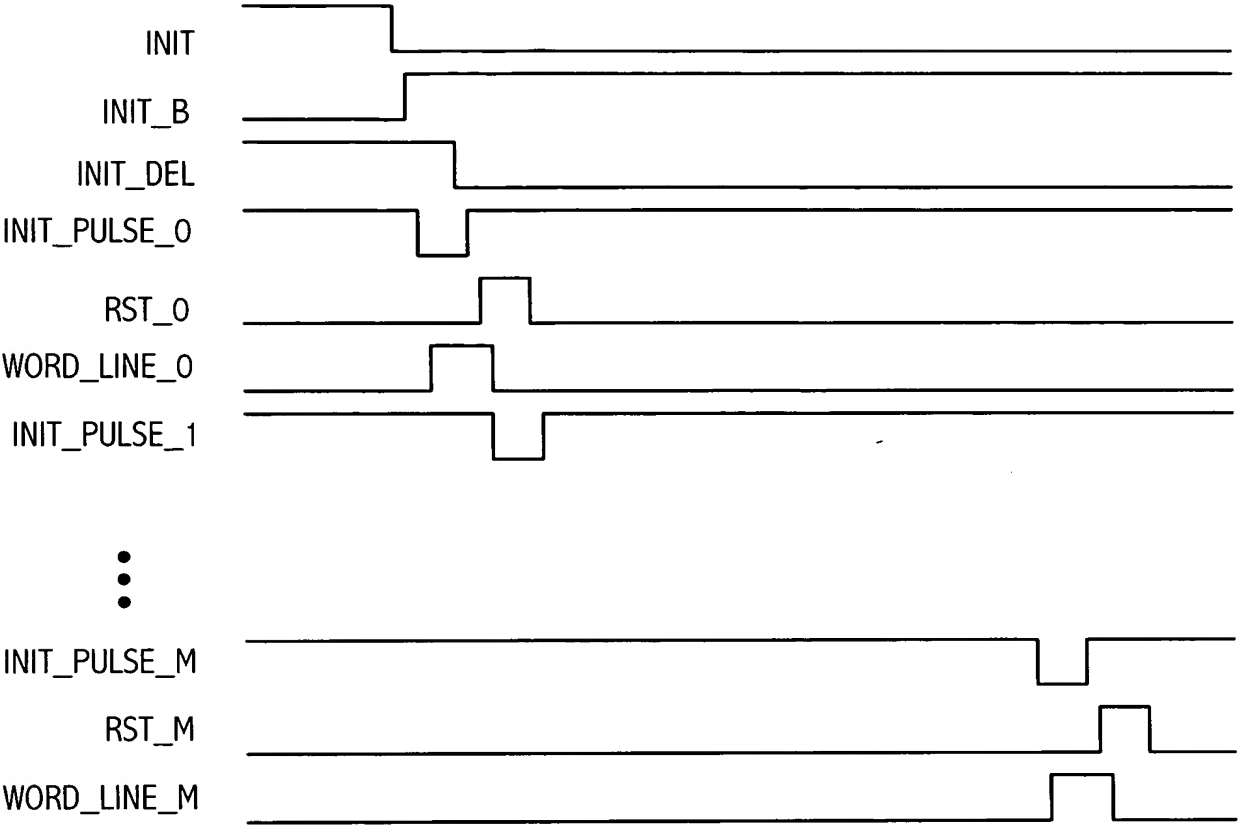


FIG. 3A

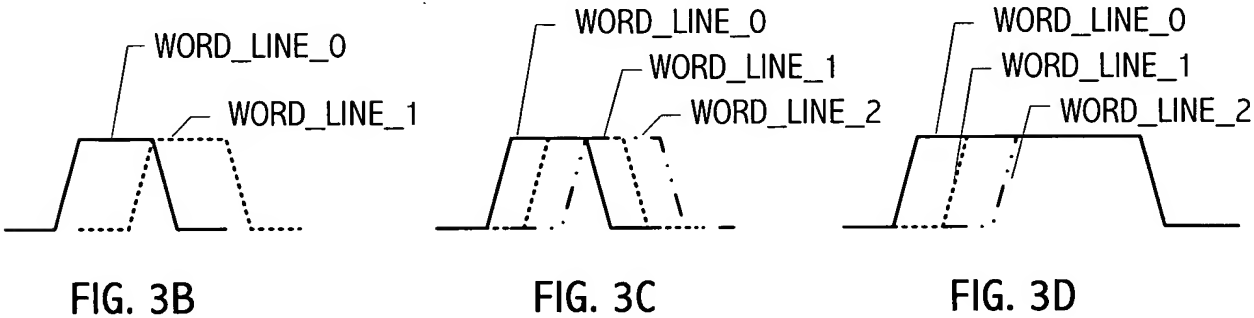


FIG. 3B

FIG. 3C

FIG. 3D

400 →

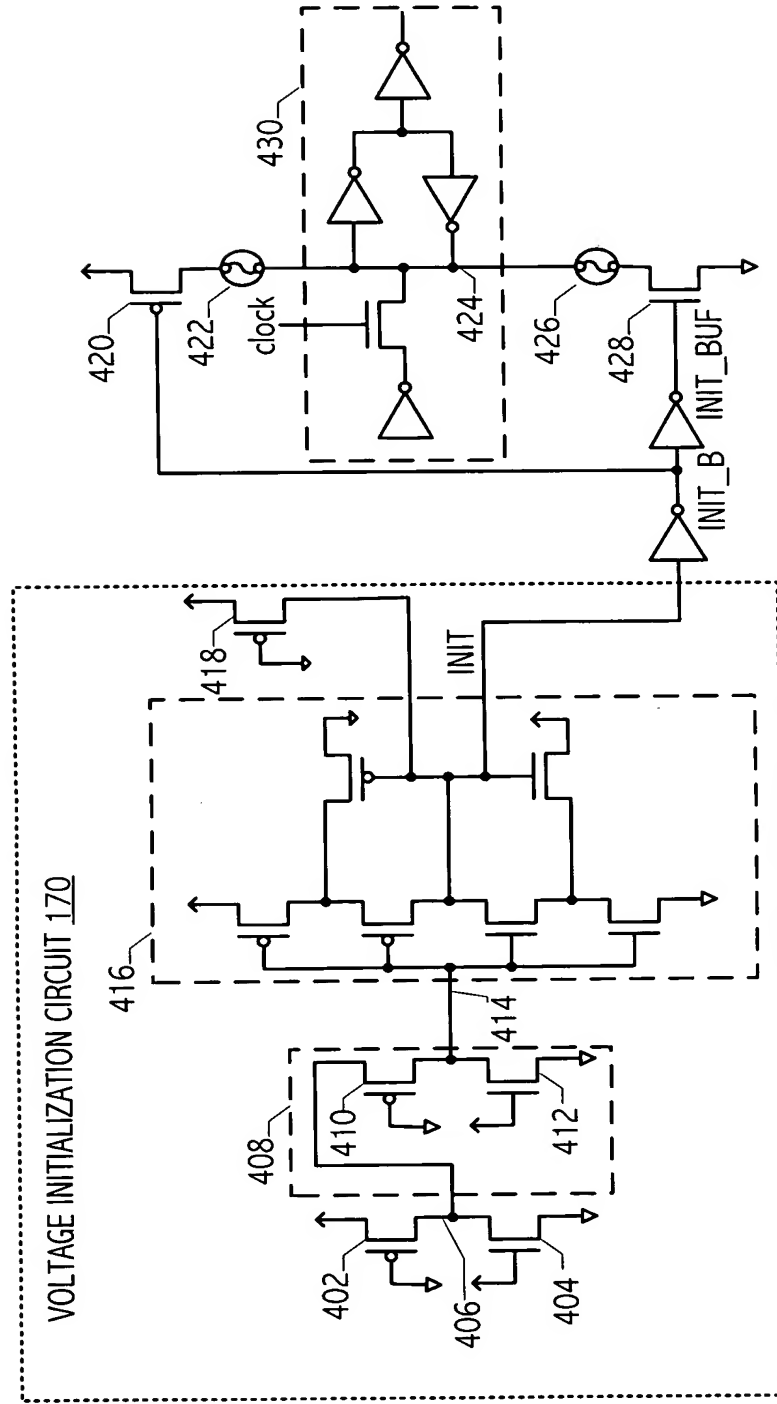


FIG. 4

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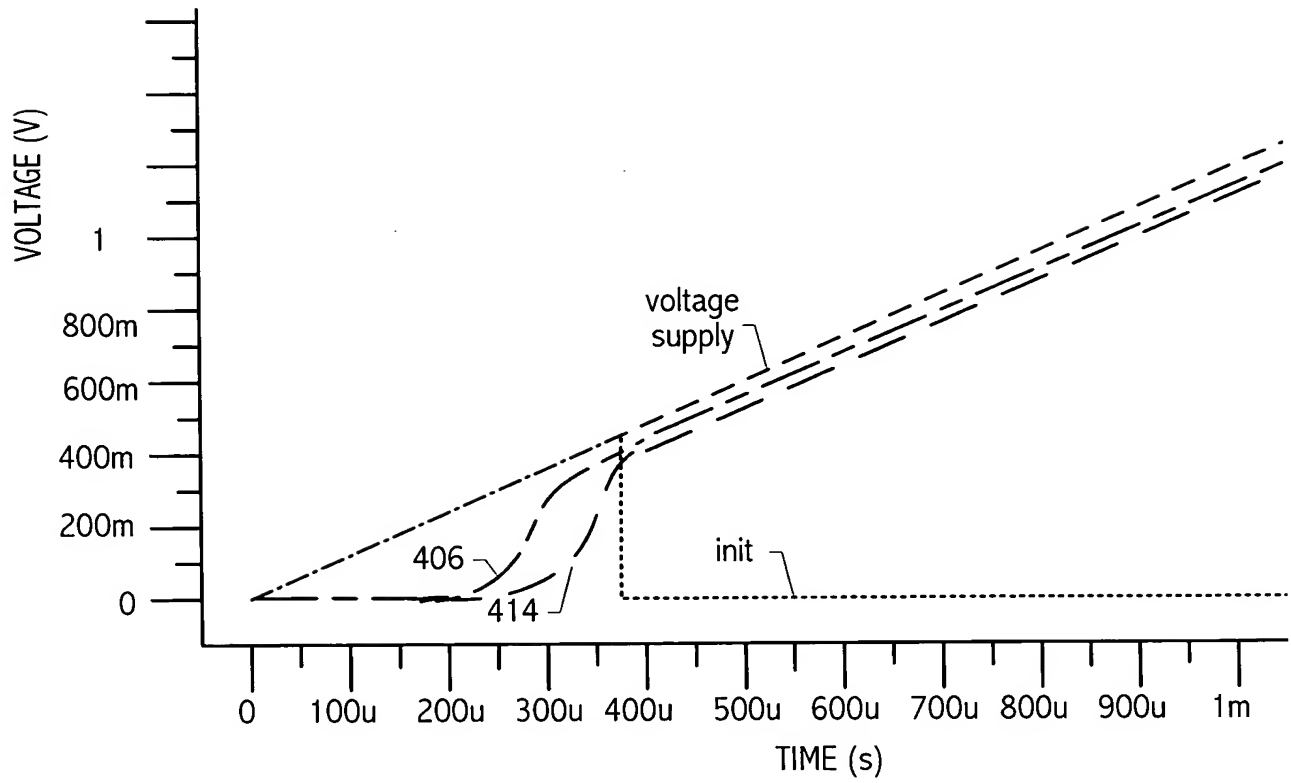


FIG. 5

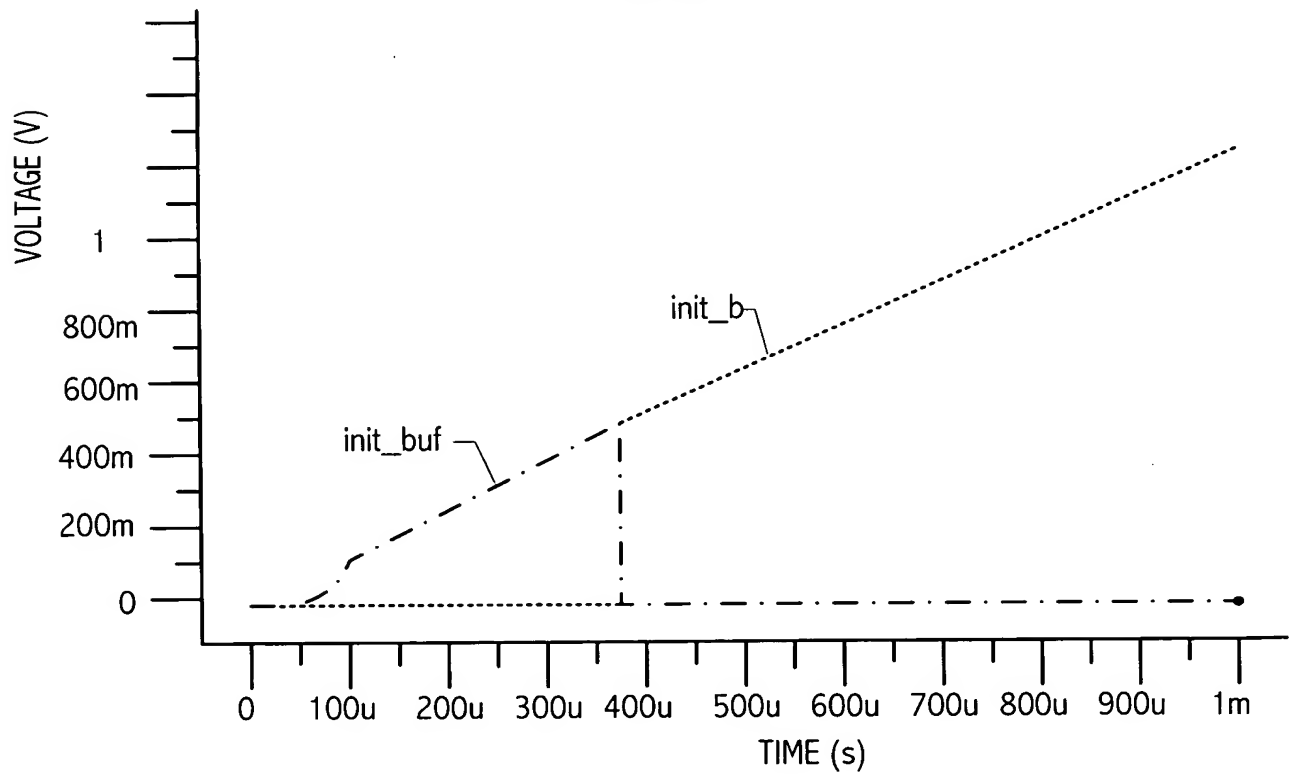


FIG. 6

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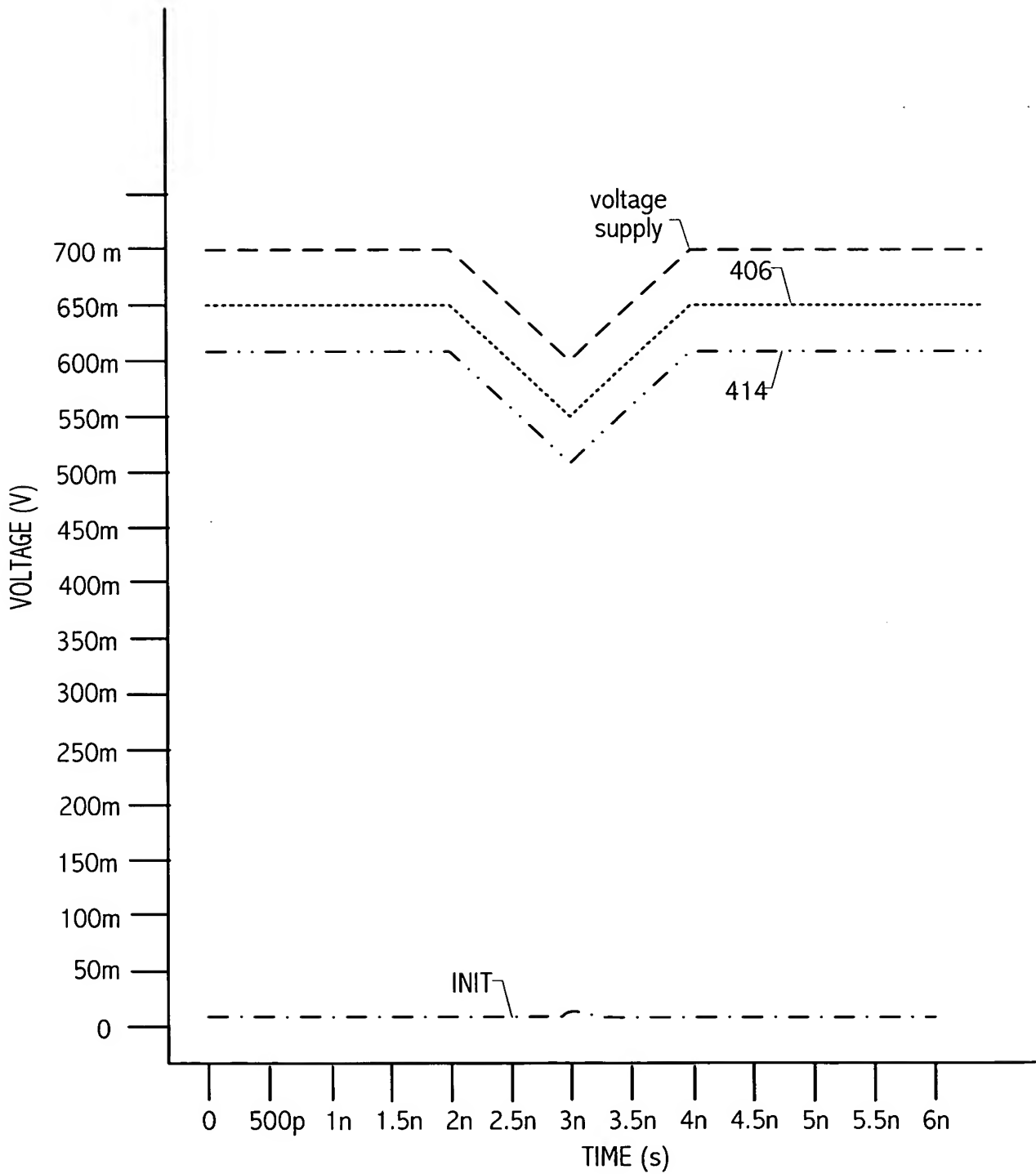


FIG. 7